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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/750,190	12/30/2003	Carlos J. Gonzalez	SNDK.334US0	9150

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PARSONS HSUE & DE RUNTZ, LLP
Suite 1800
655 Montgomery Street
San Francisco, CA 94111

EXAMINER

LI, ZHUO H

ART UNIT PAPER NUMBER

2185

DATE MAILED: 06/30/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

DETAILED ACTION

Election/Restrictions

1. Restriction to one of the following inventions is required under 35 U.S.C. 121:
 - I. Claims 1-5, and 13, drawn to a structure of a non-volatile memory system comprising an array of non-volatile memory cells with different interleaving arrangements, classified in class 711, subclass 157.
 - II. Claims 6-7, drawn to a method of operating a non-volatile memory for programming into the memory a specified number of units data less than a total data storage capacity of one block in each of the plurality of sub-arrays and having sequential logical address, and the specified number of units of data being programmed relative to the total data storage capacity of one block in each of the sub-arrays, classified in class 711, subclass 172.
 - III. Claims 8-12, drawn to method of operating a non-volatile memory for data being written and read with each of at least a first and second degrees of parallelism in response to at least one characteristic of the received write request, classified in class 711, subclass 168.

The inventions are distinct, each from the other because of the following reasons:

2. Inventions I, II and III are related as subcombinations disclosed as usable together in a single combination. The subcombinations are distinct if they do not overlap in scope and are not obvious variants, and if it is shown that at least one subcombination is separately usable. In the

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instant case, invention I has separate utility such as a structure of a non-volatile memory system comprising an array of non-volatile memory cells with different interleaving arrangements, invention II has a separate utility such as to programming into the memory a specified number of units data less than a total data storage capacity of one block in each of the plurality of sub-arrays and having sequential logical address, and the specified number of units of data being programmed relative to the total data storage capacity of one block in each of the sub-arrays, and invention III has a separate utility such as to write and read data with each of at least a first and second degrees of parallelism in response to at least one characteristic of the received write request. See MPEP § 806.05(d).

3. Because these inventions are independent or distinct for the reasons given above and have acquired a separate status in the art in view of their different classification, restriction for examination purposes as indicated is proper.
4. Applicant is advised that the reply to this requirement to be complete must include (i) an election of a species or invention to be examined even though the requirement be traversed (37 CFR 1.143).
5. Applicant is reminded that upon the cancellation of claims to a non-elected invention, the inventorship must be amended in compliance with 37 CFR 1.48(b) if one or more of the currently named inventors is no longer an inventor of at least one claim remaining in the

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application. Any amendment of inventorship must be accompanied by a request under 37 CFR 1.48(b) and by the fee required under 37 CFR 1.17(i).

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Zhuo H. Li whose telephone number is 571-272-4183. The examiner can normally be reached on Tues - Fri 9:00am - 6:30pm and alternate Monday..

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Matthew Kim can be reached on 571-272-4182. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

7. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Zhuo H. Li



Patent Examiner
June 21, 2006



MATTHEW KIM
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2100